



212881US-2524-57-2

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF:

EIJI YOSHIDA

: EXAMINER: JOHANNES P. MONDT

SERIAL NO: 09/931,104

:

FILED: AUGUST 17, 2001

: GROUP ART UNIT: 2826

FOR: SEMICONDUCTOR DEVICE AND
METHOD OF ANALYZING SAME

REQUEST FOR RECONSIDERATION

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

Please reconsider the Office Action dated March 18, 2003 in view of the following
remarks:

REMARKS

Claims 1, 6-7, and 11 remain active in application. In the outstanding Office Action, claims 1 and 6 were rejected under 35 USC 102(b) as being anticipated by U.S. Patent No. 6,084,267 to Petrosino; claim 7 was rejected under 35 USC 103(a) as being unpatentable over Petrosino in view of the Kido et al. publication; and claim 11 was rejected under 35 USC 103(a) as being unpatentable over Petrosino, or in the alternative, over Petrosino in view of "the prior art as admitted by Applicant."

First, applicants acknowledge with the appreciation the courtesy of an interview granted to Applicant's attorney on May 07, 2003 at which time the subject invention was explained in light of Applicant's disclosure and the outstanding issues were discussed. Although no agreement was reached, the Examiner noted that "it appears that one aspect in Petrosino is that the second gate is not necessarily electrically insulated from the first one,

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